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Patent

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Date: May 15, 2009

In re Application of: Toshio Sunaga Filed: 05/08/2006

or: Semiconductor Storage Device Confirmation 4737

and Burst Operation Method No.

Serial Number: 10/595,742

Art Unit: 2827 Examiner: PHAM, LY D

DO NOT ENTER: /LP/

05/21/2009 **REPLY AND AMENDMENT UNDER 35 U.S.C. §1.116**

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313

DO NOT ENTER: /LP/

05/21/2009

Sir:

This is in response to a final Office Action mailed 3/17/2009, which is due for response by 5/17/2008. Any fees required in entering this response may be charged to Applicant's deposit account, 09-0456.

It is respectfully requested that this Amendment be entered in the above referenced application and reconsideration of the application in view of these comments be made. No new matter has been included. The application should be amended as follows: